



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Andrea Pagni et al.
Application No. : 10/776,024
Filed : February 10, 2004
For : PROCESS FOR TRANSLATING INSTRUCTIONS FOR AN ARM-
TYPE PROCESSOR INTO INSTRUCTIONS FOR A LX-TYPE
PROCESSOR; RELATIVE TRANSLATOR DEVICE AND
COMPUTER PROGRAM PRODUCT

Art Unit : 2183
Docket No. : 851763.447
Date : July 7, 2004

Mail Stop Missing Parts
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

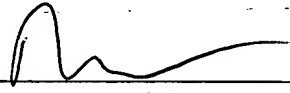
Commissioner for Patents:

In accordance with 37 C.F.R. §§ 1.56 and 1.97 through 1.98, applicants wish to make known to the U.S. Patent and Trademark Office the references set forth on the attached Form PTO-1449. Applicants note that since the application filing date is after June 30, 2003, copies of the cited U.S. patents and published patent applications are not required and accordingly have not been provided. Copies of all other cited references are enclosed. As to any reference cited, applicants do not admit that it is "prior art" under 35 U.S.C. §§ 102 or 103, and specifically reserve the right to traverse or antedate any such reference, as by a showing under 37 C.F.R. § 1.131 or other method. Although the aforesaid references are made known to the Patent and Trademark Office in compliance with applicants' duty to disclose all information they are aware of which is believed relevant to the examination of the above-identified application, applicants believe that their invention is patentable.

Please acknowledge receipt of this Information Disclosure Statement and kindly make the cited references of record in the above-identified application.

Applicants believe this Information Disclosure Statement has been timely filed, however, the Director is authorized to charge any fee due by way of this Information Disclosure Statement to our Deposit Account No. 19-1090.

Respectfully submitted,
Seed Intellectual Property Law Group PLLC



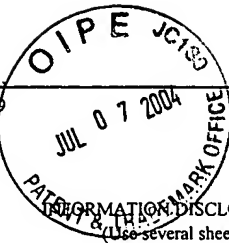
Robert Iannucci
Registration No. 33,514

Enclosures:

Postcard
Form PTO-1449
Cited References (3)

701 Fifth Avenue, Suite 6300
Seattle, Washington 98104-7092
Phone: (206) 622-4900
Fax: (206) 682-6031

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FORM PTO-1449
(REV. 7-80)U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.
851763.447APPLICATION NO.
10/776,024APPLICANTS
Andrea Pagni et al.FILING DATE
February 10, 2004GROUP ART UNIT
2183INFORMATION DISCLOSURE STATEMENT
(Use several sheets if necessary)

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION	
					YES	NO
	AK	1 028 370 A2	08/16/00	EP		
	AL	WO 02/086699 A2	10/31/02	WIPO		
	AM					
	AN					
	AO					

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	AP	IBM Corp., "Real-Time CISC Architecture HW Emulator on a RISC Processor," <i>IBM Technical Disclosure Bulletin</i> 37(3):605, March 1994.
	AQ	
	AR	

EXAMINER

DATE CONSIDERED

* EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).